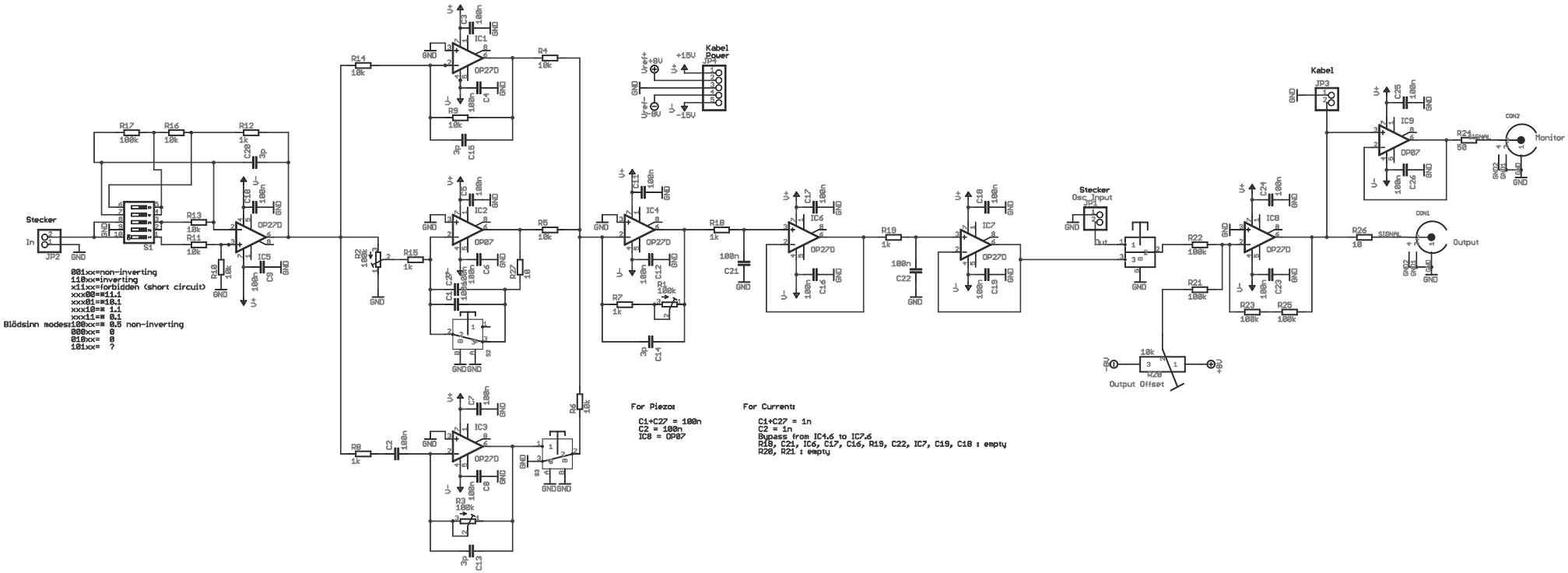
Stecker
In
JP2
GND
S1
R17
100k
R16
10k
R12
1k
C20
3p
R13
10k
R11
10k
R10
10k
C10
100n
IC5
OP27D
C3
001xx=non-inverting
110xx=inverting
x11xx=forbidden (short circuit)
xxx00=x11.1
xxx01=x10.1
xxx10=x 1.1
xxx11=x 0.1
Blödsinn modes100xx=x 0.5 non-inverting
000xx= 0
010xx= 0
101xx= ?
R14
10k
IC1
OP27D
C1
C3
100n
R4
10k
R9
10k
C15
3p
IC2
OP07
C5
C4
C27
3p
R27
10
R5
10k
D2
100k
R15
1k
IC3
OP27D
C8
C7
C2
R8
1k
R3
100k
C13
3p
R6
10k
S3
S2
Kabel
Power
JP4
Vref+
+8V
Vref-
-8V
+15V
-15V
IC4
OP27D
C11
C12
R1
100k
R7
1k
C14
3p
R18
1k
C21
IC6
OP27D
C17
C16
R19
1k
C22
IC7
OP27D
C18
C19
Stecker
Osc Input
JP1
Out
R22
100k
R21
100k
10k
R20
Output Offset
IC8
OP27D
C24
R23
100k
R25
100k
Kabel
JP3
IC9
OP07
C25
C26
R24
50
SIGNAL
CON2
Monitor
R26
10
CON1
Output
For Piezos
C1+C27 = 100n
C2 = 100n
IC8 = OP07
For Currents
C1+C27 = in
C2 = in
Bypass from IC4.6 to IC7.6
R18, C21, IC6, C17, C16, R19, C22, IC7, C19, C18 : empty
R20, R21 : empty