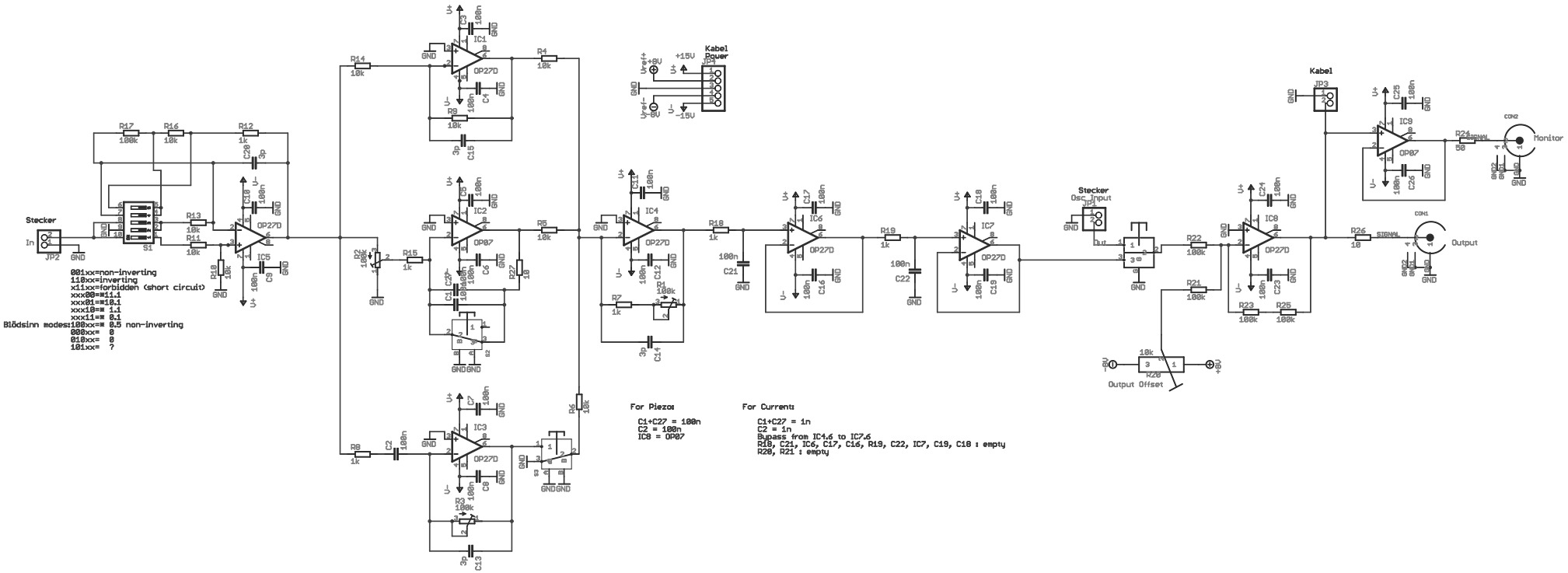
Stecker
In
JP2
GND
S1
R17 100k
R16 10k
R12 1k
C20 3p
R13 10k
R14 10k
R18 10k
C10 100n
IC5
OP27D
C8 100n
001xx=non-inverting
110xx=inverting
x11xx=forbidden (short circuit)
xxx00=*11.1
xxx01=*10.1
xxx10=* 1.1
xxx11=* 0.1
Blödsinn modes:100xx=* 0.5 non-inverting
000xx= 0
010xx= 0
101xx= ?
R14 10k
R4 10k
IC1
OP27D
R9 10k
3p C15
R2 100k
R15 1k
IC2
OP07
R5 10k
C5 100n
R27 10
C27 100n
R1 100k
GND GND
R6 10k
R8 1k
C2 100n
IC3
OP27D
C9 100n
R3 100k
3p C13
Vref+ +8V
Vref- -8V
+15V
-15V
Kabel Power
JP4
IC4
OP27D
C11 100n
C12 100n
R7 1k
R11 100k
3p C14
R18 1k
100n C21
IC6
OP27D
C17 100n
C16 100n
R19 1k
100n C22
IC7
OP27D
C18 100n
C19 100n
For Piezos
C1+C27 = 100n
C2 = 100n
IC8 = OP07
For Currents
C1+C27 = in
C2 = in
Bypass from IC4.6 to IC7.6
R18, C21, IC6, C17, C16, R19, C22, IC7, C19, C18 : empty
R20, R21 : empty
Stecker
Osc. Input
JP1
Out
GND
R22 100k
R21 100k
10k
R20
-8V
+8V
Output Offset
IC8
OP27D
C24 100n
C23 100n
R23 100k
R25 100k
Kabel
JP3
IC9
OP07
C25 100n
C26 100n
R24 50
SIGNAL
CON2
Monitor
R26 10
CON1
Output