

Precision Optocoupler

Function

An optocoupler is used to isolate a voltage source from the process (or experiment) by creating an optical link between the two using an LED-photodiode combination. A digital optocoupler can output only digital signals (high or low), while an analog optocoupler faithfully reproduces the input voltage into the same output voltage but now produced by an independent power source. This device provides good electrical isolation of the power source (typically, an analog output card of a computer) from potentially harmful processes that might occur on the experiment side (eg: voltage spikes during fast switching of magnetic fields).



The circuit and the board is a replica of a project by Todd Meyrath which can be found [here](#). The central optocoupler circuit was inspired by fig. 17 of the [datasheet of the HCNR200](#).

The original project required two independent +/- power supplies. This revamped version (2024) can be powered from a single +/- power supply by utilizing an isolated DC-DC converter (plus sophisticated filtering) to power the secondary side. If signal quality is absolutely important and any kind of residual noise introduced by the DC-DC converter (<1 mV) one can power the device as before from two independent (linear) +/- power supplies.

Properties



- Bandwidth is roughly 200-250 kHz (step-response: 4 μ s delay)
- RMS ripple on the output should be around 2-3 mV

Date

Project date (v2.0): July 2024

Responsible

Klaus Zipfel (VLBAI)

Circuit diagram

The project is managed and hosted (at the moment) in the [VLBAI gitlab instance](#). Access can be granted by VLBAI members. Alternatively, the project might be moved to the [IQ Git](#)

- The [schematic](#) in PDF-Format
- The [production](#) files (gerber, cpl, bom)
- The [Layout](#) in HTML format (ibom)

Test

Input and output signals should ideally match 100% (offset and amplitude). If not, adjust via the three trimmers until they match.

Kalkulation



was	wieviel	E-Preis	Preis	Anmerkung
Leiterplatte	1x	??.?? €	??.?? €	1/n von XXX EUR
Gehäuse	1x	??.?? €	??.?? €	
*	?x	??.?? €	??.?? €	...
R,C	??x	0.02 €	0.22 €	Bauform 0805
Bestückung			??.00 €	bei SRM
Verschnitt			?..?? €	
	Summe		??.?? €	

Date Project date: August 2011

Responsible Hrishikesh Kelkar, kelkar@iqo.uni-hannover.de

Circuit diagram

- The [schematic](#) in PDF-Format
- The Eagle source files can be obtained from the [Download-page](#) of wiki. (Direkt-Link: [Schematic](#), [Board](#))

Test Eingangssignale sollten sich eins-zu eins am Ausgang wieder finden. Bandbreite ist etwa 10 kHz.

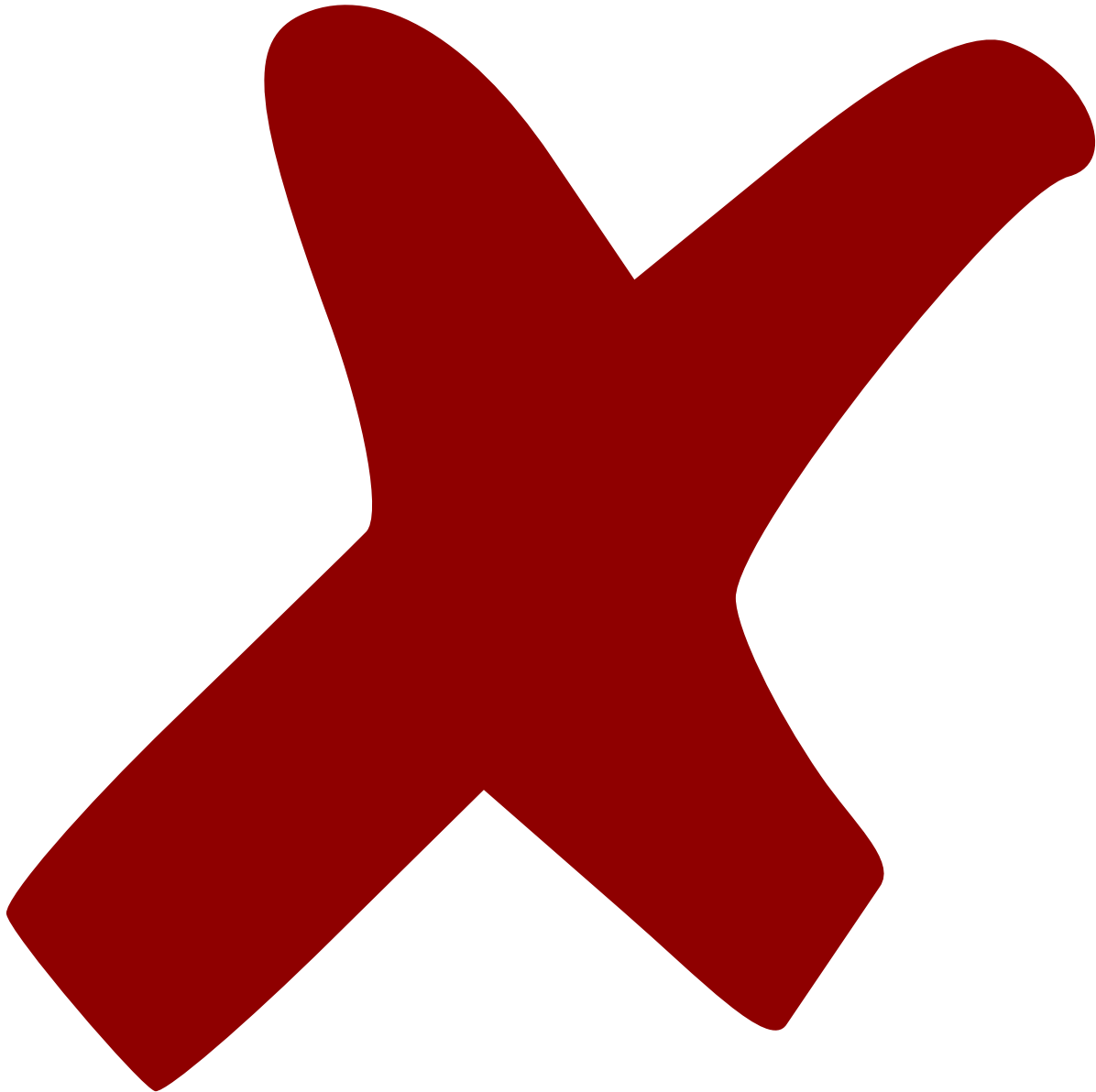
Kalkulation

 **Fix Me!**

was	wieviel	E-Preis	Preis	Anmerkung
Leiterplatte	1x	??.?? €	??.?? €	1/n von XXX EUR
Gehäuse	1x	??.?? €	??.?? €	
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R,C	??x	0.02 €	0.22 €	Bauform 0805
Bestückung			??.00 €	bei SRM
Verschnitt			?..? €	
	Summe		??.?? €	

Meckerliste

Was für die nächste Version zu tun ist: (



: verworfen,



: in Arbeit,



Schaltplan, aber noch nicht im Layout, : im



: erledigt)

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Last update: **2024/07/31 11:26**

